

**LISTING OF CLAIMS:**

This listing of claims will replace all prior versions and listings, of claims in the present application:

1. (Currently Amended) An RF structure comprising:  
a dielectric material having metal inductor wires of a first thickness and a metal bond pad having a major area of a second thickness both are located on a surface thereof, wherein said first thickness is greater than said second thickness and said dielectric material represents a final interconnect level of an interconnect structure.
2. (Original) The RF structure of Claim 1 wherein the metal inductor wires and the metal bond pad are both composed of a conductive metal that has a resistivity of about 3 micro-ohms\*cm or less.
3. (Original) The RF structure of Claim 2 wherein the conductive metal is selected from the group consisting of Cu, Al, Pt, Ag, Au, and alloys thereof.
4. (Original) The RF structure of Claim 1 wherein the metal inductor wires and the metal bond pad are both composed of Al.
5. (Original) The RF structure of Claim 1 wherein the first thickness is from about 2000 to about 5000 nm.

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6. (Original) The RF structure of Claim 1 wherein the second thickness is from about 500 to about 1500 nm.

7. (Original) The RF structure of Claim 1 wherein the dielectric layer is an insulator having a dielectric constant less than 4.0 or an insulator having a dielectric constant of about 4.0 or greater.

8. (Original) The RF structure of Claim 1 wherein the dielectric material is selected from oxides nitrides, oxynitrides, polyimides, polyimines and Si-containing polymers.

9. (Original) The RF structure of Claim 1 further comprising a passivation layer located on exposed walls of said metal wire inductor and portions of said metal bond pad.

10. (Withdrawn) A method of fabricating an RF structure including an optimized padset and a high performance inductor comprising:

forming a metal layer on a surface of a dielectric material; and

patterning said metal layer so as to form metal inductor wires of a first thickness and a metal bond pad having a major area of a second thickness located on a surface of said dielectric material, wherein said first thickness is greater than said second thickness.

11. (Withdrawn) The method of Claim 10 wherein the patterning comprises first providing a structure having metal inductor wires and a metal bond pad of the first thickness and then selectively etching the metal bond pad to thin said metal bond pad to said second thickness.

12. (Withdrawn) The method of Claim 11 wherein the etching comprises a timed etching process.

13. (Withdrawn) The method of Claim 11 wherein the providing the structure having metal inductor wires and a metal bond pad of the first thickness includes an inductor wire patterning step.

14. (Withdrawn) The method of Claim 11 further comprising forming a passivation layer on surfaces of said structure having metal inductor wires and a metal bond pad of the first thickness.

15. (Withdrawn) The method of Claim 14 wherein said passivation layer atop the metal bond pad is removed prior to said etching.

16. (Withdrawn) The method of Claim 10 wherein said patterning comprises wire bond pad photolithography patterning, wire bond pad etching to form said wire bond pad of said second thickness, and inductor wire photolithography and etching.

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17. (Withdrawn) The method of Claim 10 further comprising forming a conductive wire in contact with said wire bond pad having said second thickness.

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